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LSI Logic Corporation
Legal Department - IP
1621 Barber Lane, MS D-106
Milpitas, CA 95035Date December 8, 2005Number of pages including cover sheet 2

To: US PTO

Mail Stop Status Request

TC 2123

Fax No. 571-273-8300

Phone No.

CC:

From: Connie del Castillo

Intellectual Property Paralegal

Telephone (408) 433-7191
No.

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REMARKS:

☐ Urgent ☐ For your review ☒ Reply ASAP ☐ Please commentApplication Number: 10/736,386
Filing date: December 15, 2003
First named inventor: Bruce Whitefield et al.
Attorney docket number: 03-1345

Transmitted herewith for filing via facsimile:

- Status Request Letter

Pursuant to 37 C.F.R. 1.8, I hereby certify that this correspondence is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date indicated below.

Date

12/8/05

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Connie del Castillo

Connie del Castillo

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Bruce Whitefield et. al.

Serial No. :

10/736,386

Group Art Unit : 2123

Filed :

December 15, 2003

Examiner :

For :

Method For Calculating High-Resolution Wafer Parameter Profiles

Atty Docket : / 03-1345

STATUS REQUEST

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir,

Applicants request a status of the above referenced application. Please mail the status notification to the address stated below.

Respectfully submitted,

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Date: 5 Dec 05

24.6.17

Timothy Croll, Ph: [+1] 408-433-7191

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